

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2826  
Examiner: Teresa M. Arroyo  
Confirmation No.: 5593

In Re PATENT APPLICATION Of:

Applicant: Kazumasa TANIDA et al.

Serial No.: 10/594,561

Filed:: September 27, 2006

For: SEMICONDUCTOR DEVICE WITH A  
SEMICONDUCTOR CHIP  
CONNECTED IN A FLIP CHIP  
MANNER

Attorney Ref.: AI 427NP

**RESPONSE TO  
ELECTION/RESTRICTION  
REQUIREMENT**

June 29, 2011

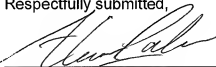
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the further Election/Restriction Requirement mailed on May 31, 2011, Applicants further elect Group I, drawn to a semiconductor device classified in class 257, subclass 778, i.e. the subject matter of claims 16-37, for further examination. The election is made without traverse.

Examination of the application on the merits respectfully is requested.

Respectfully submitted,



June 29, 2011  
Date

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